



#5 B. L. sp/or
SHEET 1 OF 1

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 218357US2		SERIAL NO. 10/052,259	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Yoshiaki SHIMOOKA, et al.			
				FILING DATE January 23, 2002		GROUP 2812	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
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	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
TL	AV	Y. ODATE, et al., IEEE, Electronic Components and Technology Conference, pages 526-529, "A MEASUREMENT AND SIMULATION STUDY OF TRANSMISSION LINES ON MICROSTRIP AND STACKED-PAIR STRUCTURE FOR HIGH SPEED SIGNALS", May, 2000					
TL	AW	T. SUGA, Nikkei Microdevice, Part 5 Package Technology, pages 136-143, "THERE IS NO SOLUTION ONLY WITH LSI", July, 2000 (with Partial English translation, pages 1-10)					
	AX						
	AY						
	AZ						
Examiner <i>[Signature]</i>					Date Considered 04/22/03		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

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